

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :



Vikram Shroffy
Santhanakris Raman

Serial No. : 10/632,622

Group Art Unit : 2825

Filed : July 31, 2003

Examiner : Garbowski, Leigh Marie

For : Method for Providing Clock-Net
Aware Dummy Metal Using
Dummy Regions

Atty Docket : 2889P / 03-0873

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Connie Del Castillo

10/11/05

Date

Connie Del Castillo

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milipitas, CA 95035
408-433-7475

Respectfully submitted,

TC

Date: 10 Oct 05

Timothy Croll

Reg. No. 36,771